

FIG. 1

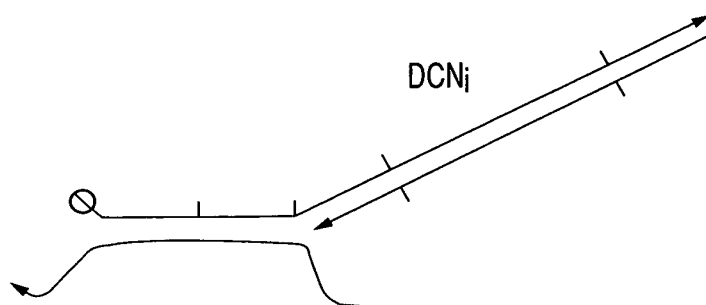


FIG. 2

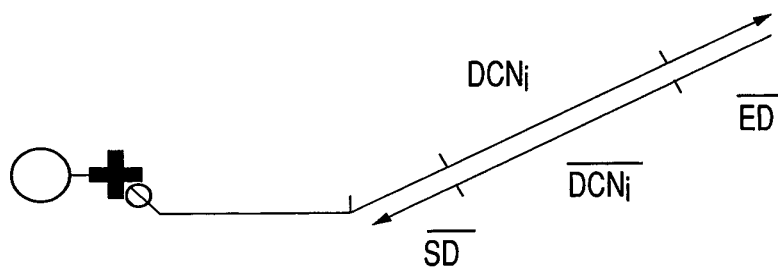


FIG. 3

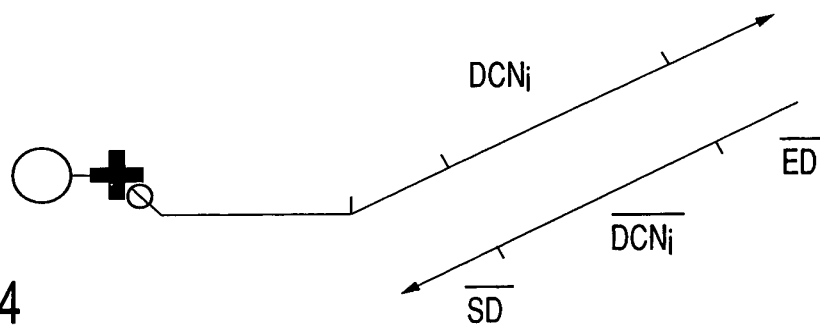


FIG. 4

FIG. 5

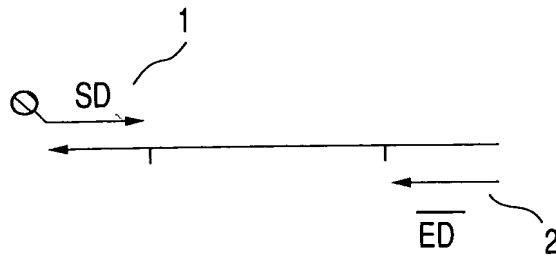


FIG. 6

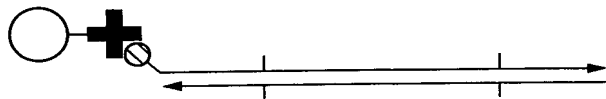


FIG. 7

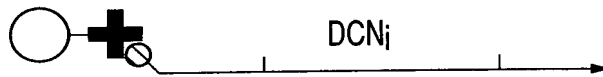


FIG. 8

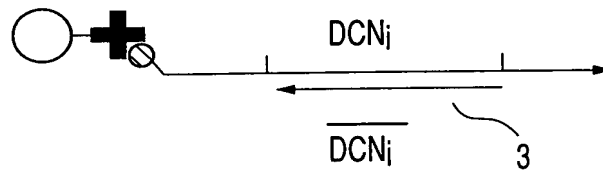


FIG. 9

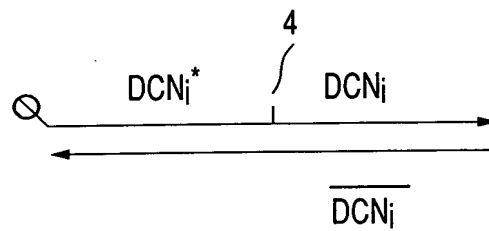


FIG. 10

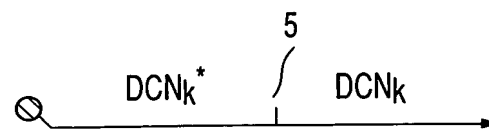


FIG. 11

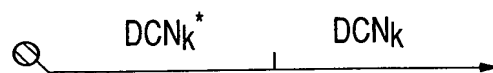


FIG. 12

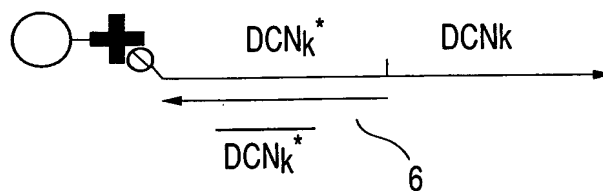


FIG. 13

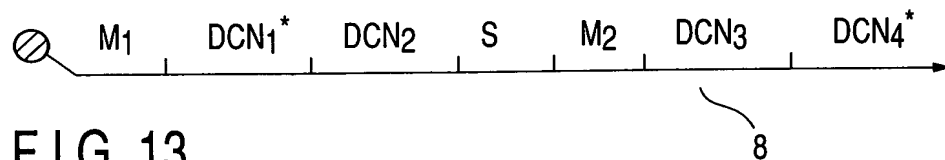


FIG. 14

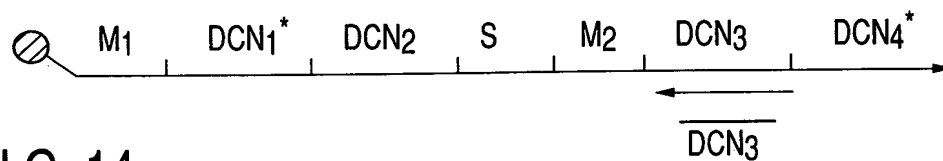


FIG. 15

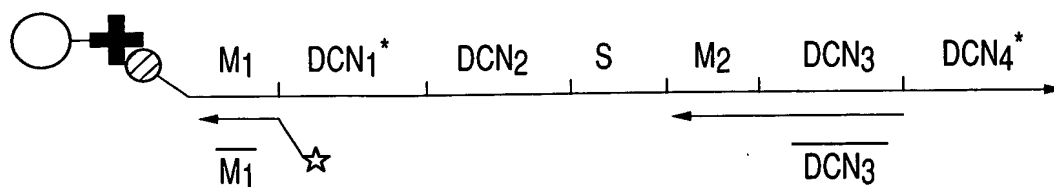
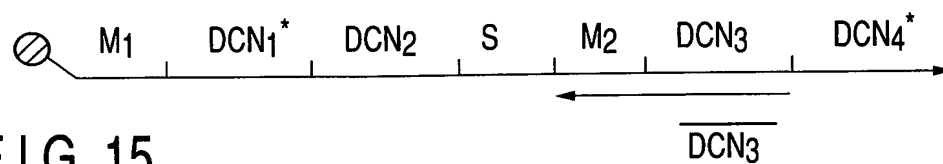


FIG. 16

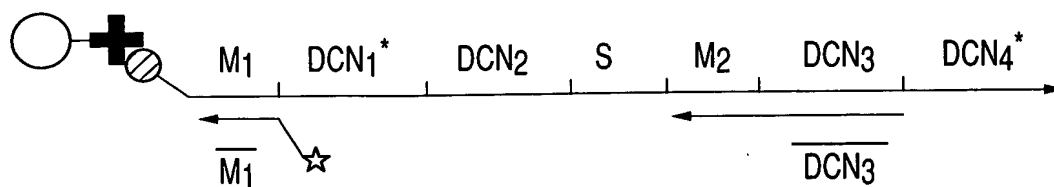


FIG. 17

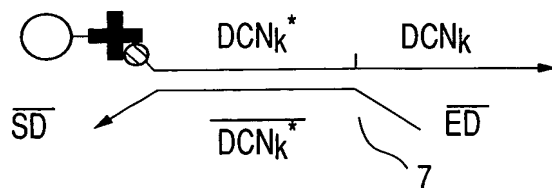


FIG. 18

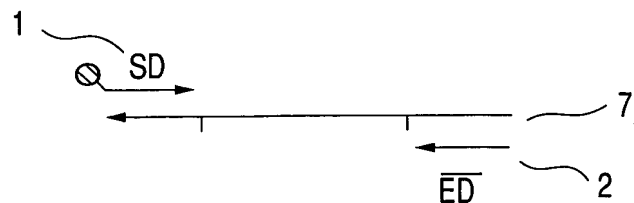


FIG. 19

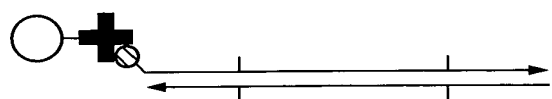


FIG. 20

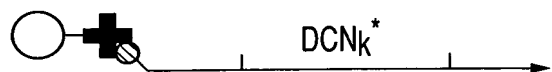


FIG. 21

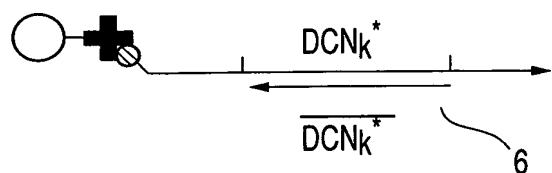


FIG. 22

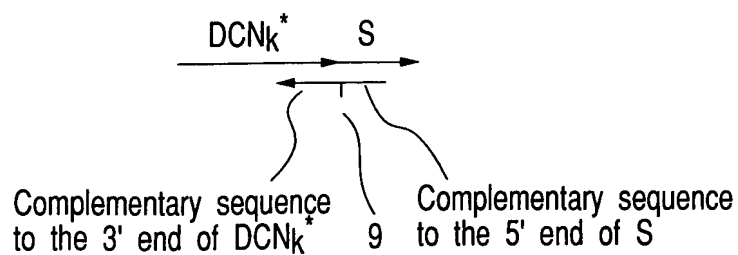
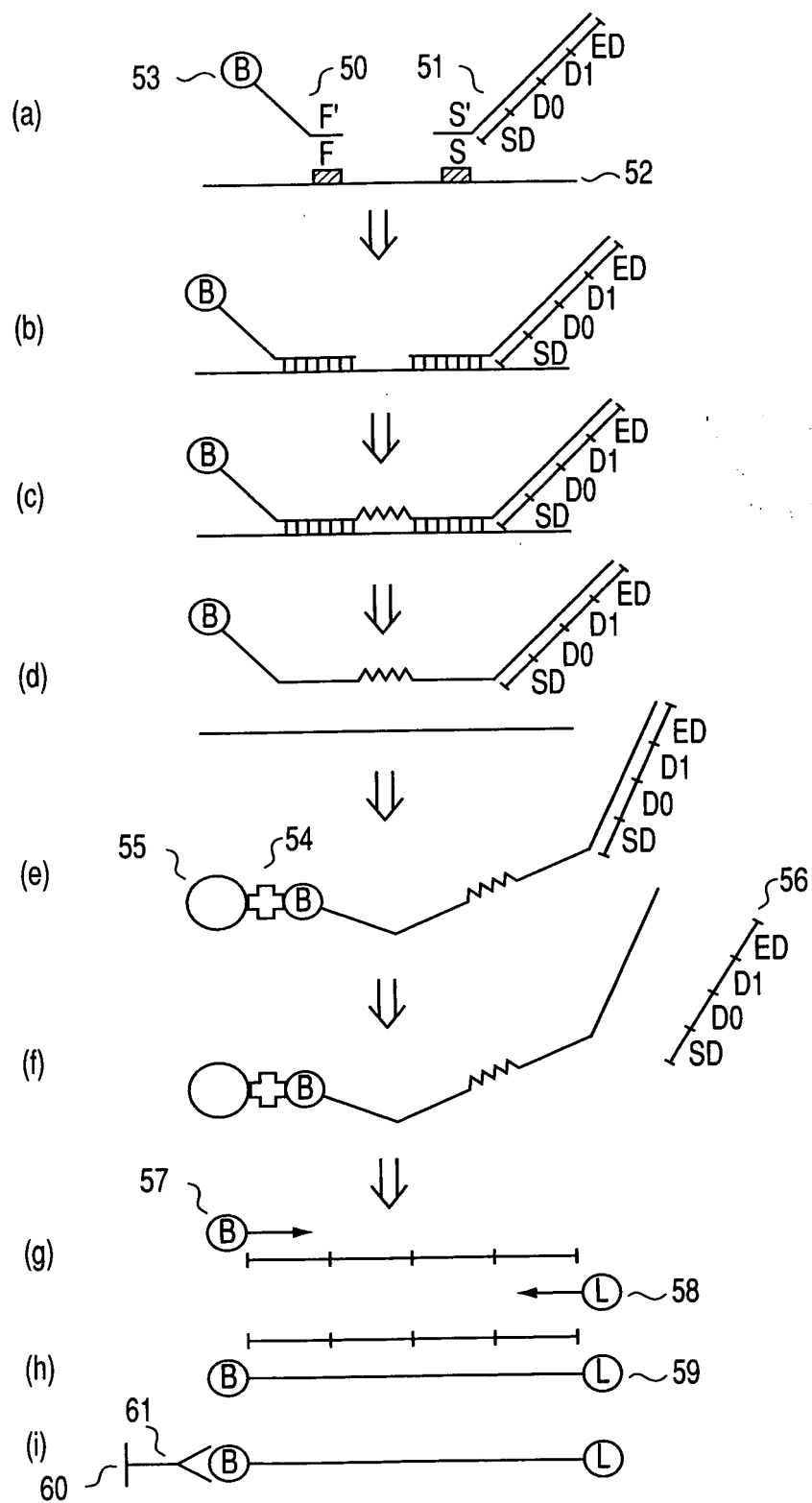
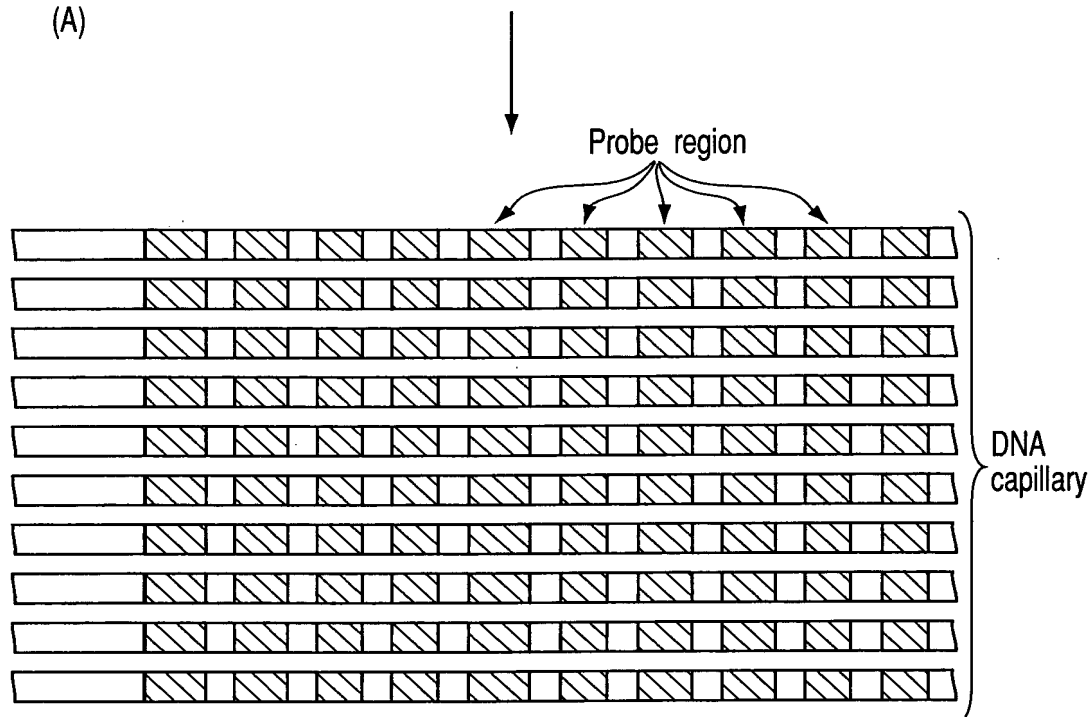


FIG. 23



		D0									
		D0-1	D0-2	D0-3	D0-4	D0-5	D0-6	D0-7	D0-8	D0-9	D0-10
D1	D1-1	1									
	D1-2										
	D1-3										
	D1-4										
	D1-5										
	D1-6										
	D1-7										
	D1-8										
	D1-9										
	D1-10										

(A)



(B)

FIG. 24

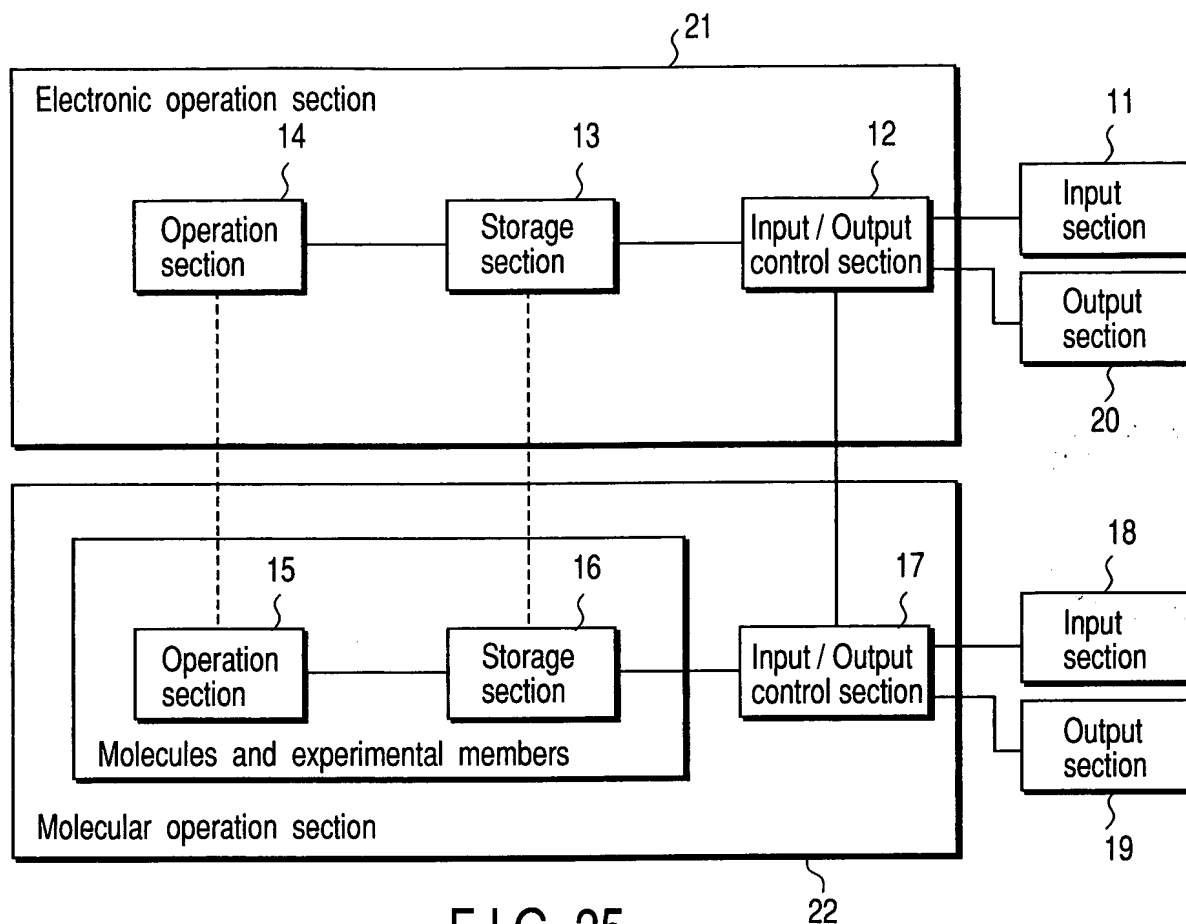


FIG. 25

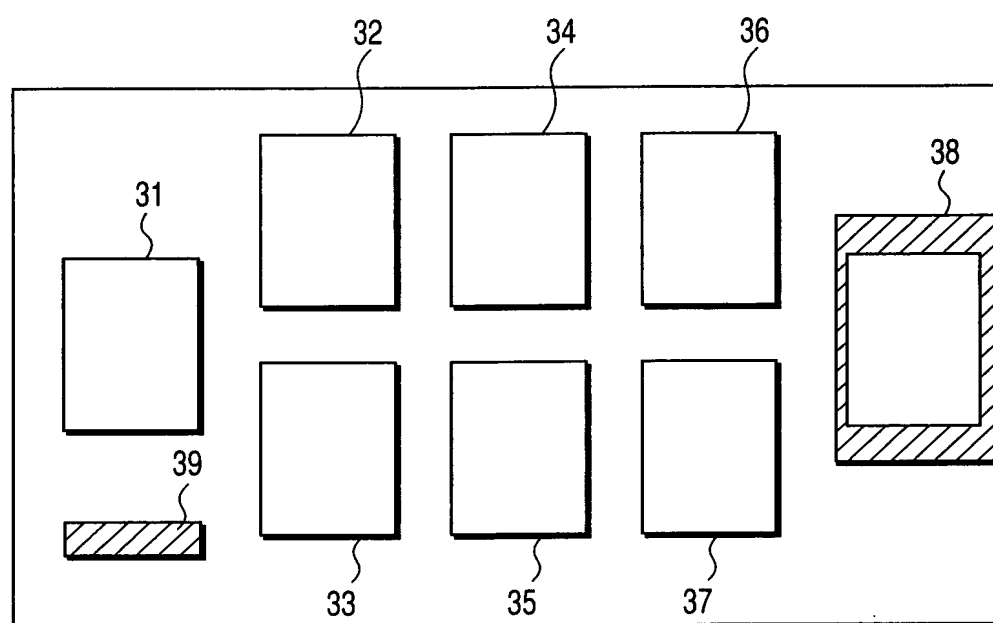


FIG. 28

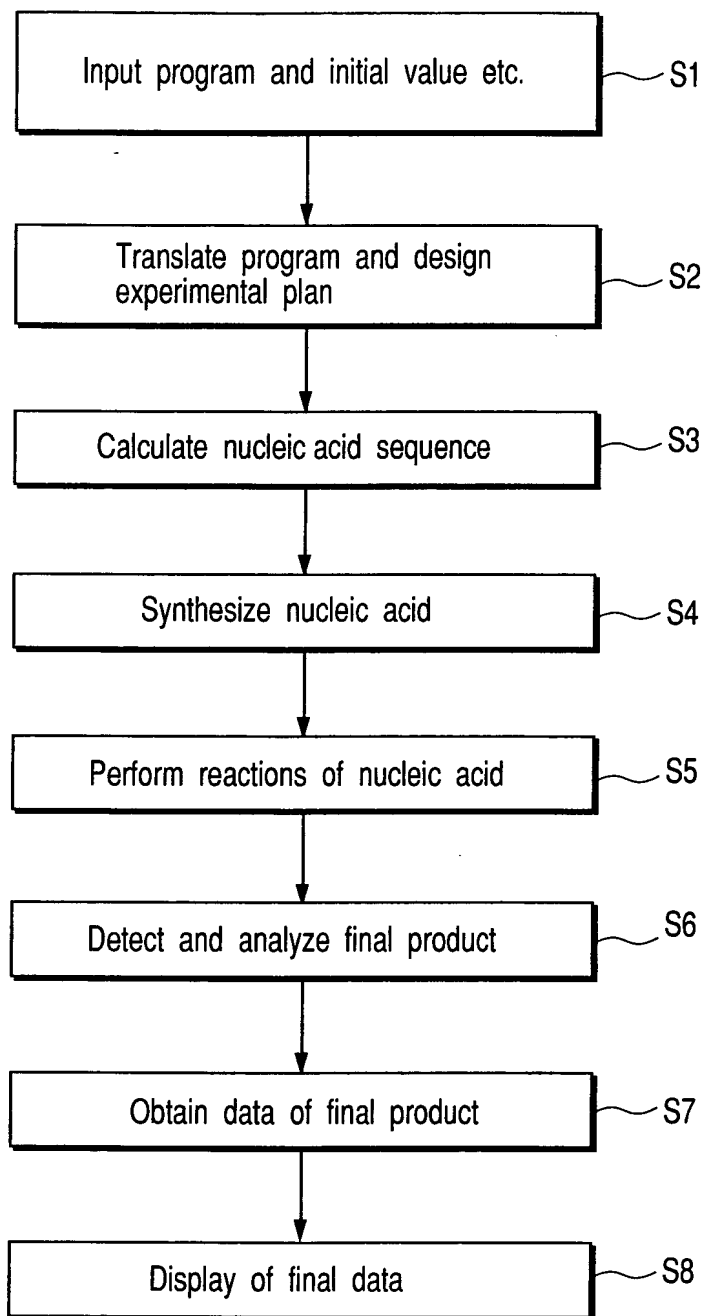


FIG. 26

Structure of device

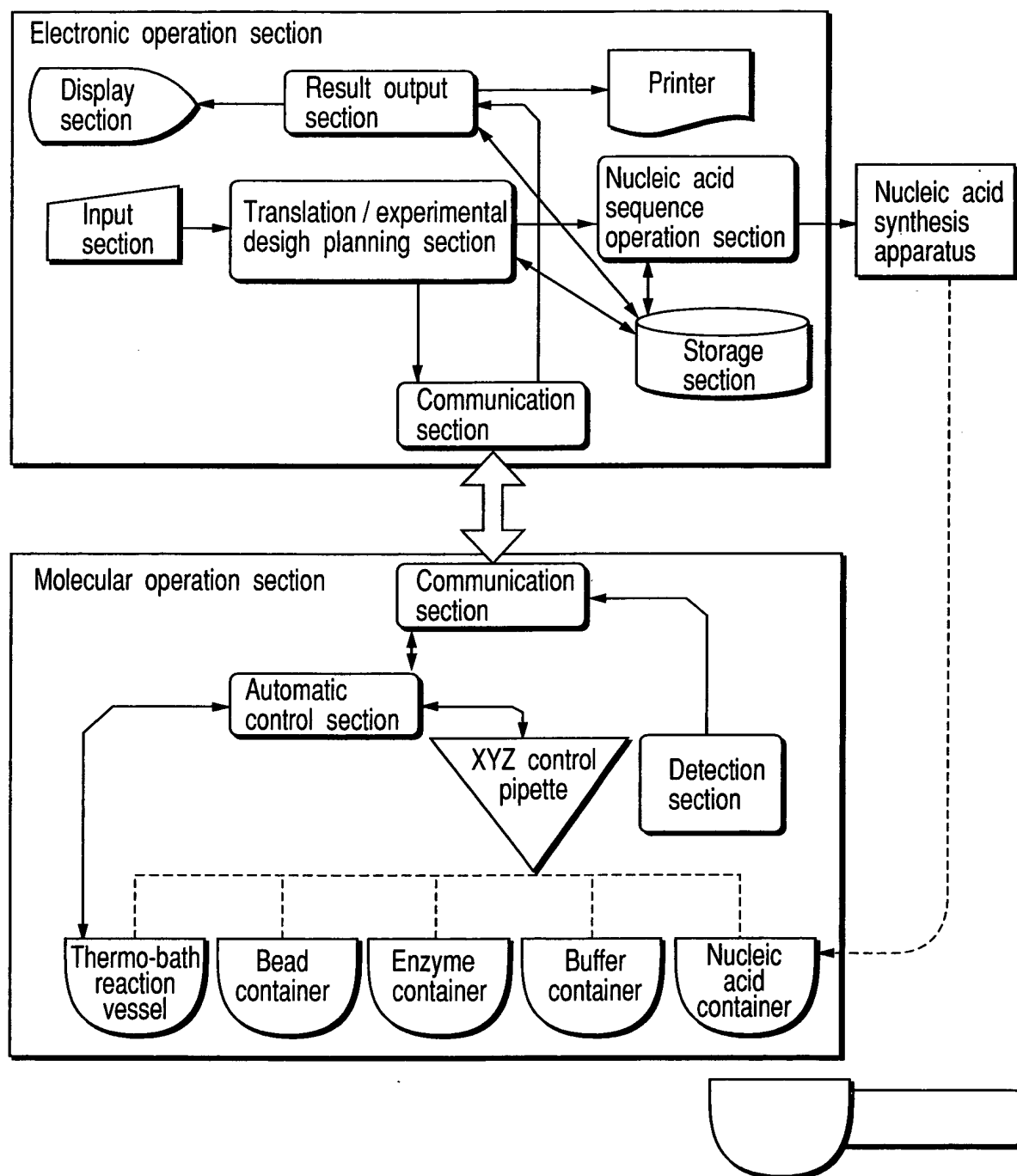


FIG. 27

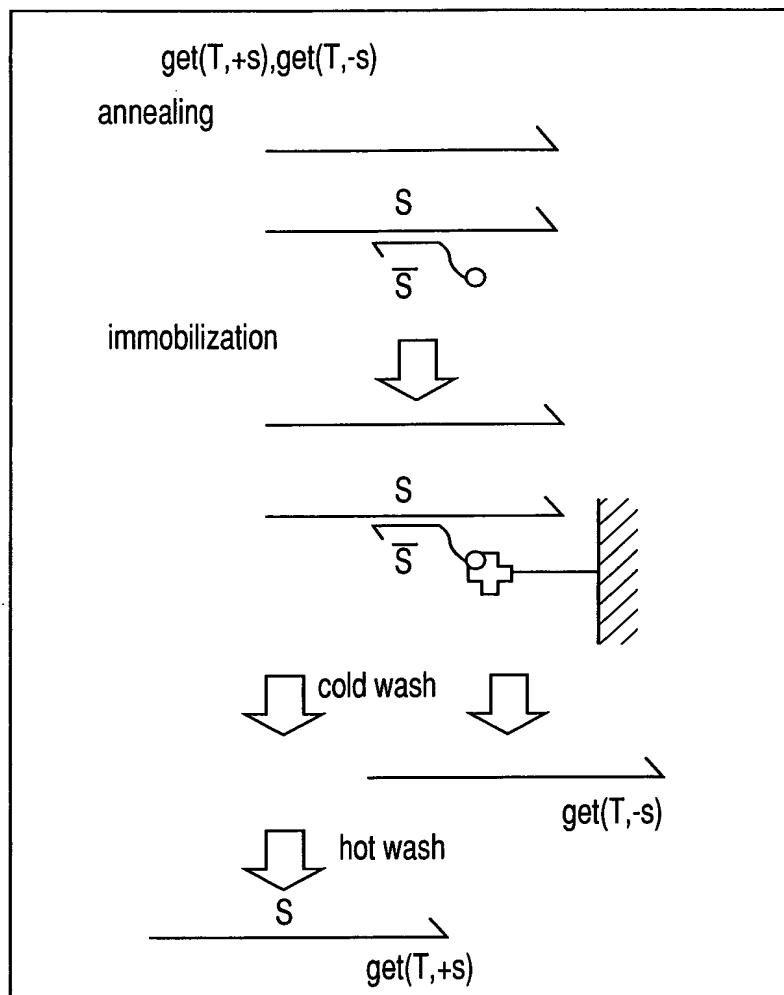


FIG. 29

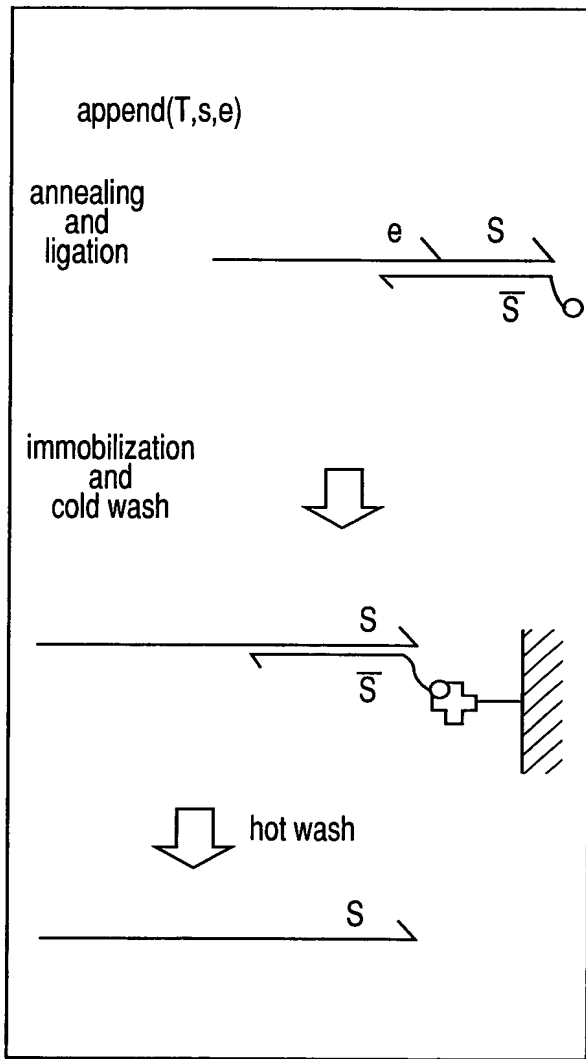


FIG. 30

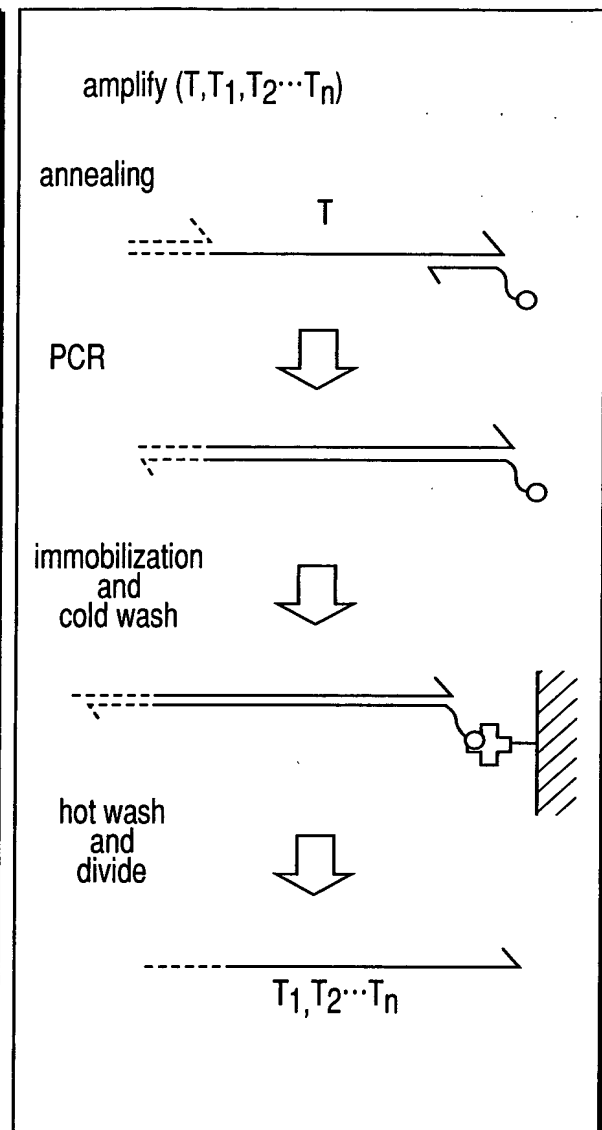


FIG. 31

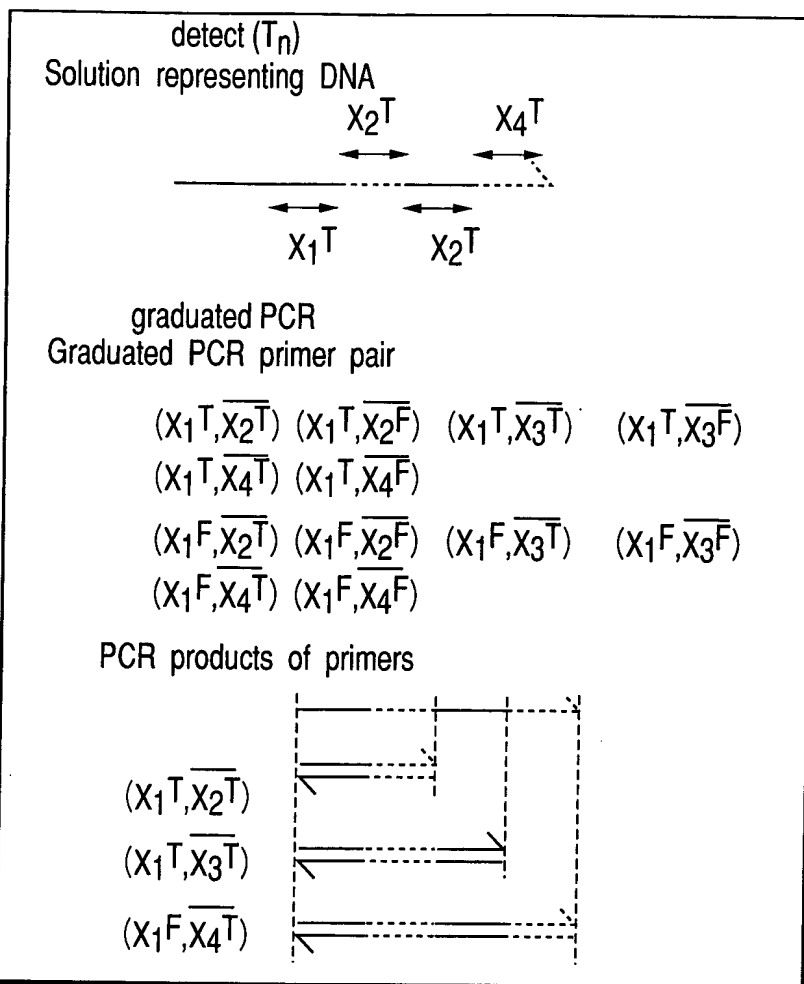


FIG. 32

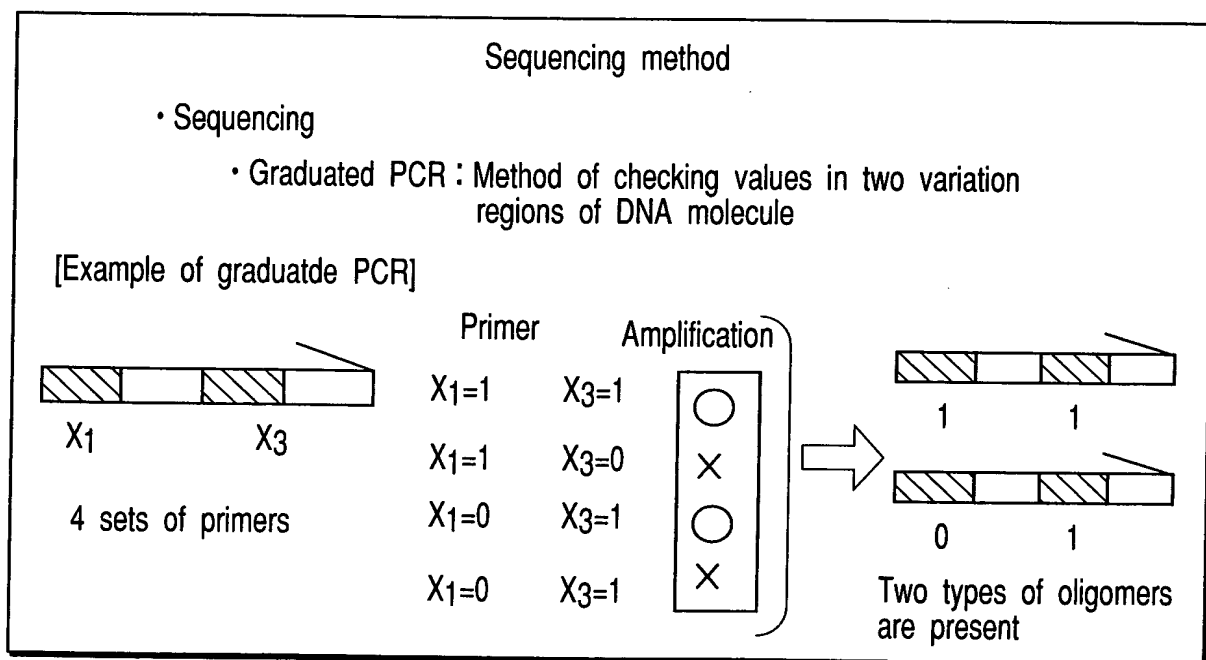


FIG. 34

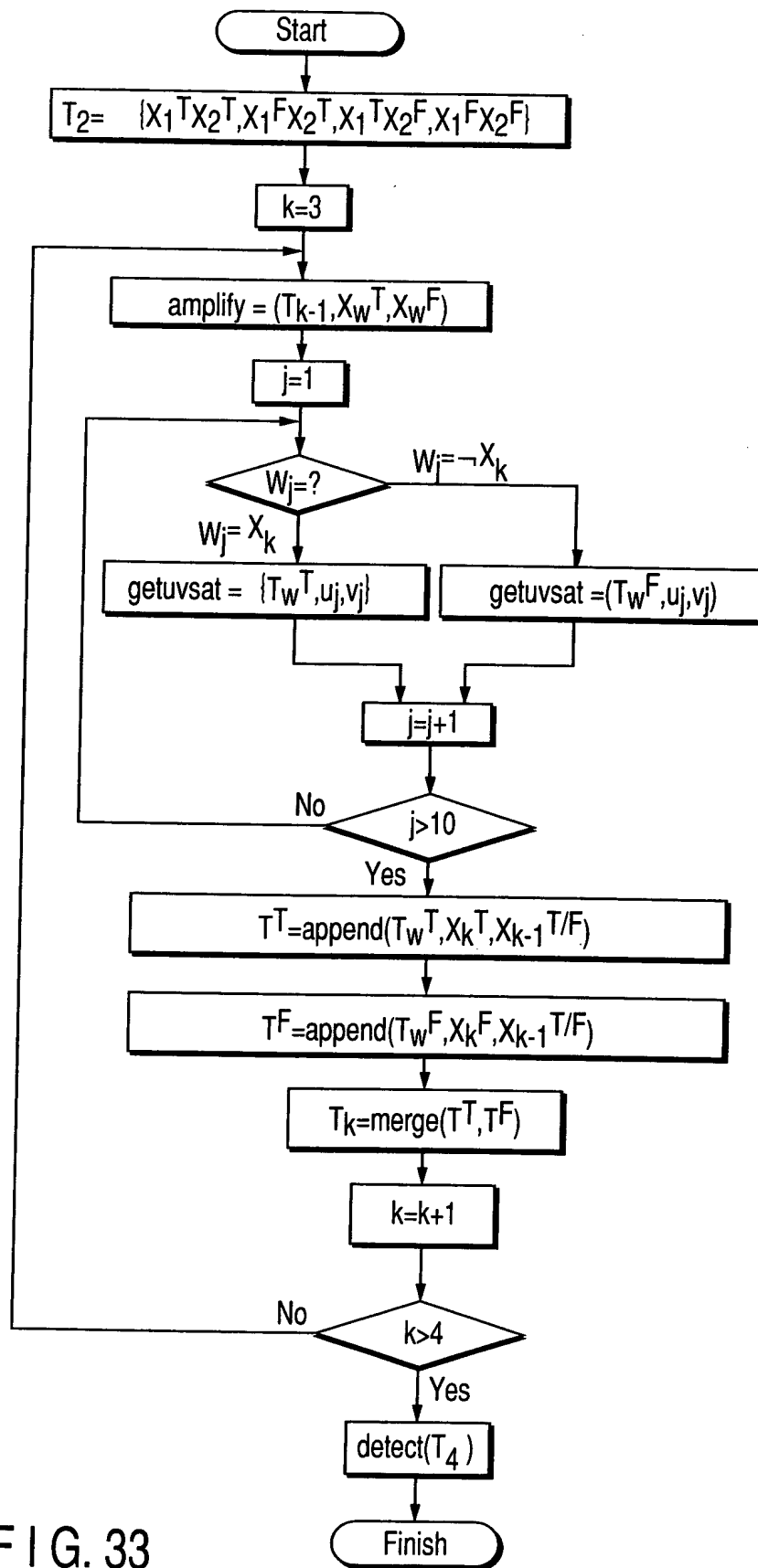


FIG. 33

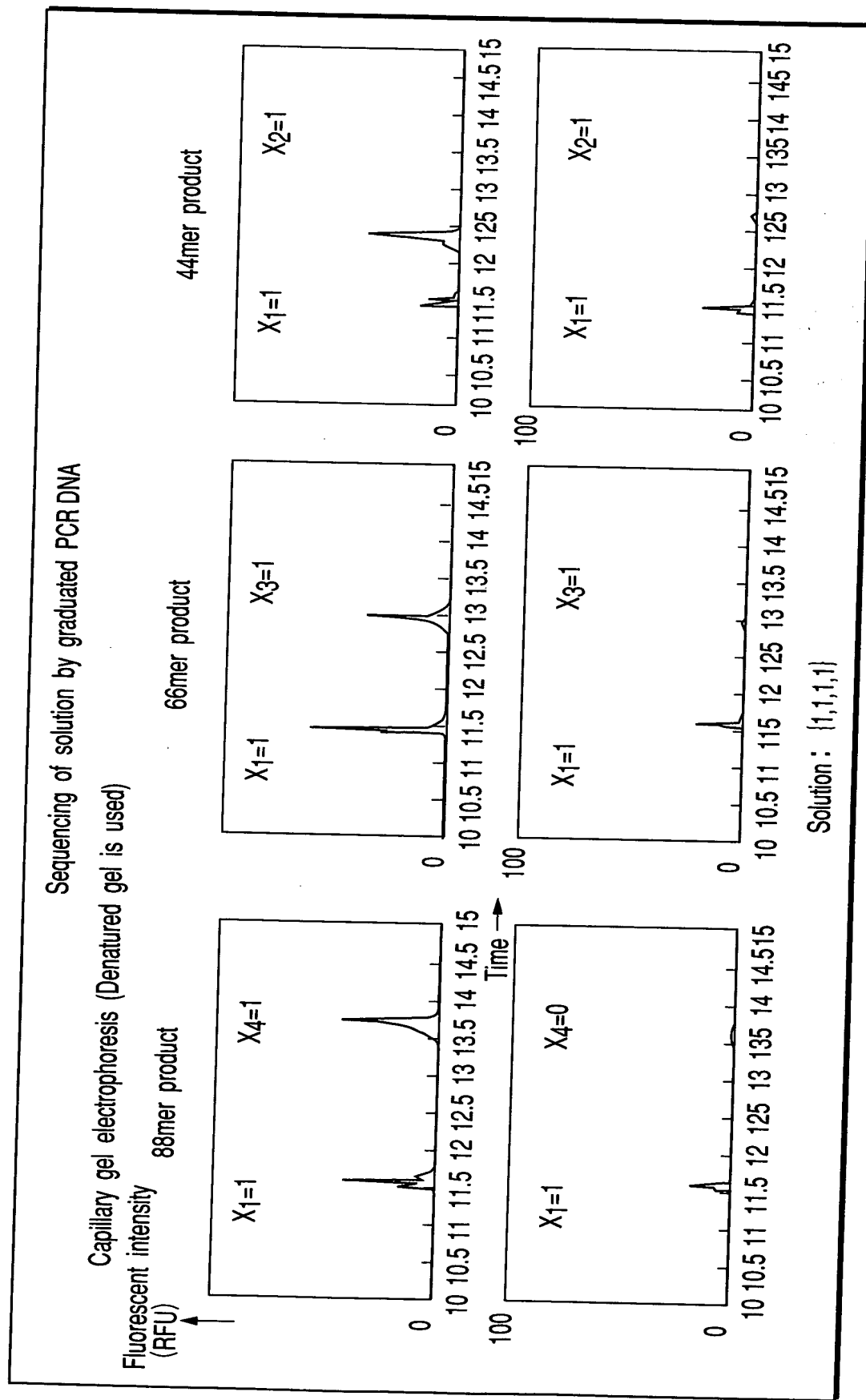


FIG. 35